

JC4/920

J1093 U.S. PTO  
09/966721  
09/27/03

Subclass	Class	ISSUE CLASSIFICATION

PATENT NUMBER

## U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

HP  
SCANNED 11/13/03 Q.A. WB

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/966721	F	438	106	2812	Zhou

APPLICANTS  
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(55)

2827

TITLE  
Structure of a semiconductor module and method of manufacturing the samePTO-2040  
12/99

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____					
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Primary Examiner) _____ (Date) _____ (Legal Instruments Examiner) _____ (Date) _____			ISSUE FEE	
				Amount Due	
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